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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10550323
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	<del>2434</del> 2162	
	Examiner Name	<del>M.T. Henning</del> JBF	
	Attorney Docket Number	53047-57370	

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	2	3611314	A	1971-10-05	Pritchard, Jr. et al.	
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	4	3824375	A	1974-07-16	Gross et al.	
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9	4314356	A	1982-02-02	Scarbrough	
10	4385393	A	1983-05-24	Chaure et al.	
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5	20030093347	A1	2003-05-15	Gray	
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2	"Overview, Field Programmable Port Extender", January 2002 Gigabit Workshop Tutorial, Washington University, St. Louis, MO, January 3-4, 2002. <del>All Pages.</del>	<input type="checkbox"/>
3	"Payload Plus <sup>TM</sup> Agere System Interface", Agere Systems Product Brief, June 2001, downloaded from Internet, January 2002. <del>All Pages.</del>	<input type="checkbox"/>
4	"The Field-Programmable Port Extender (FPX)", downloaded from <a href="http://www.arl.wustl.edu/arl/">http://www.arl.wustl.edu/arl/</a> in March 2002. <del>All Pages.</del>	<input type="checkbox"/>
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13	COMPTON et al.; "Configurable Computing: A Survey of Systems and Software"; Technical Report, Northwestern University, Dept. of ECE, 1999. <del>All Pages</del> .	<input type="checkbox"/>
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24	International Search Report for PCT/US2001/011255; July 10, 2003 .All Pages .	<input type="checkbox"/>
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